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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Vincent Dupaquis et al. PATENT APPLICATION
Serial No.: 10/781,311 Group Art Unit: 2131
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METHOD AND HARDWARE THEREFOR

Supplemental Information Disclosure Statement
with Certification under 37 CFR § 1.97(e)(1)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The following information is submitted in compliance with Applicants' duty of disclosure under 37 CFR § 1.97(e). A copy of each of the cited references is enclosed.

OTHER REFERENCES

The Chinese Remainder Theorem and Its Application in a High-Speed RSA Crypto Chip by J. Grossschadl, IEEE Comput. Soc., U.S., 11 December 2000, pages 384-393, XP010529836, ISBN 0-7695-0859-6.

Approaching Encryption at ISDN Speed Using Partial Parallel Modulus Multiplication by K.C. Posch et al., Microprocessing and Microprogramming, Elsevier Science Publishers, BV., Amsterdam NL., Vol. 29, No. 3, October 1990, pages 177-184, XP000151455, ISSN: 0165-6074.

The undersigned hereby certifies that the items of information contained in this Supplemental Information Disclosure Statement were cited in a communication received from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.

CERTIFICATE OF MAILING

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22303-1450

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Respectfully submitted,

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Group: 2131

U.S. PATENT DOCUMENTS

Examiner Initial*	Document Number	Publ. Date	Name	Class	Sub Class	Filing Date
AA						
AB						
AC						
AD						
AE						
AF						
AG						
AH						
AI						
AJ						

FOREIGN PATENT DOCUMENTS

Examiner Initial*	Document Number	Publ. Date	Country	Class	Sub Class	Translation Yes No
AK						
AL						
AM						
AN						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AO	J. Grossschadel, <i>the Chinese Remainder Theorem and Its Application in a High-Speed RSA Crypto Chip</i> , 11 December 2000, IEEE Comput. Soc., U.S., pages 384-393, XP010529836. ISBN 0-7695-0859-6.
AP	K.C. Posch et al., <i>Microprocessing and Microprogramming</i> , Elsevier Science Publishers, BV, Amsterdam NL., Vol. 29, No. 3, October 1990, pages 177-184, XP000151455, ISSN: 0165-6074.

EXAMINER:

DATE CONSIDERED:

*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.